

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
1	BRS	L1	10707 2	output adj circuit	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/25 10:07	
2	BRS	L2	2267	1 and current adj drive\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/25 10:08	
3	BRS	L3	1885	2 and transistor\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/25 10:08	
4	BRS	L4	1163	3 and (inversion inverting inverter\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/25 10:09	
5	BRS	L5	411	4 and voltage near2 (compensat\$3 adjust\$3 regulat\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/25 10:09	
6	BRS	L6	252	5 and data	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/25 10:10	